

## WEST Search History

DATE: Tuesday, May 20, 2003

| <u>Set Name</u>                           | <u>Query</u>  | <u>Hit Count</u> | <u>Set Name</u> |
|---|---|------------------|-----------------|
| side by side                              |   |                  | result set      |
| <i>DB=USPT,PGPB,JPAB; PLUR=YES; OP=OR</i> |   |                  |                 |
| L10                                       | L9 and (noise or crosstalk or cross-talk or interfer\$) | 42               | L10             |
| L9  | l5 and L7   | 93               | L9              |
| L8  | l5 and l2 and L7  | 7                | L8              |
| L7  | gate same transistor same width                         | 17407            | L7              |
| L6  | l3 and l4 and L5  | 8                | L6              |
| L5  | drc or (rule same check\$)                              | 9950             | L5              |
| L4  | l2 and L3   | 307              | L4              |
| L3  | gate same simulat\$                                     | 6500             | L3              |
| L2  | width same effective                                    | 47839            | L2              |
| L1  | gate same width same (drc or (rule same check\$4))      | 28               | L1              |

END OF SEARCH HISTORY

**WEST**

Generate Collection

Print

**Search Results - Record(s) 1 through 15 of 42 returned.**☐ 1. Document ID: US 20030066038 A1

L10: Entry 1 of 42

File: PGPB

Apr 3, 2003

PGPUB-DOCUMENT-NUMBER: 20030066038

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030066038 A1

TITLE: Alleviating line end shortening by extending phase shifters

PUBLICATION-DATE: April 3, 2003

## INVENTOR-INFORMATION:

| NAME          | CITY      | STATE | COUNTRY | RULE-47 |
|---------------|-----------|-------|---------|---------|
| Ma, Melody W. | San Jose  | CA    | US      |         |
| Liu, Hua-Yu   | Palo Alto | CA    | US      |         |

US-CL-CURRENT: 716/2; 716/19, 716/4

| Full      | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KMIC |
|-----------|-------|----------|-------|--------|----------------|------|-----------|-----------|-------------|--------|------|
| Draw Desc | Image |          |       |        |                |      |           |           |             |        |      |

☒ 2. Document ID: US 20030051219 A1

L10: Entry 2 of 42

File: PGPB

Mar 13, 2003

PGPUB-DOCUMENT-NUMBER: 20030051219

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030051219 A1

TITLE: Modifying a hierarchical representation of a circuit to process composite gates

PUBLICATION-DATE: March 13, 2003

## INVENTOR-INFORMATION:

| NAME          | CITY    | STATE | COUNTRY | RULE-47 |
|---------------|---------|-------|---------|---------|
| Manoo, Masoud | Zebulon | NC    | US      |         |

US-CL-CURRENT: 716/11

| Full      | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KMIC |
|-----------|-------|----------|-------|--------|----------------|------|-----------|-----------|-------------|--------|------|
| Draw Desc | Image |          |       |        |                |      |           |           |             |        |      |

☒ 3. Document ID: US 20030049550 A1

L10: Entry 3 of 42

File: PGPB

Mar 13, 2003

PGPUB-DOCUMENT-NUMBER: 20030049550  
PGPUB-FILING-TYPE: new  
DOCUMENT-IDENTIFIER: US 20030049550 A1

TITLE: Modifying a hierarchical representation of a circuit to process features  
created by interactions between cells

PUBLICATION-DATE: March 13, 2003

## INVENTOR-INFORMATION:

| NAME          | CITY    | STATE | COUNTRY | RULE-47 |
|---------------|---------|-------|---------|---------|
| Manoo, Masoud | Zebulon | NC    | US      |         |

US-CL-CURRENT: 430/30

| Full      | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC |
|-----------|-------|----------|-------|--------|----------------|------|-----------|-----------|-------------|--------|------|
| Draw Desc | Image |          |       |        |                |      |           |           |             |        |      |

☒ 4. Document ID: US 20020197541 A1

L10: Entry 4 of 42

File: PGPB

Dec 26, 2002

PGPUB-DOCUMENT-NUMBER: 20020197541  
PGPUB-FILING-TYPE: new  
DOCUMENT-IDENTIFIER: US 20020197541 A1

TITLE: Method and apparatus for forming a pattern on an integrated circuit using  
differing exposure characteristics

PUBLICATION-DATE: December 26, 2002

## INVENTOR-INFORMATION:

| NAME               | CITY   | STATE | COUNTRY | RULE-47 |
|--------------------|--------|-------|---------|---------|
| Grobman, Warren D. | Austin | TX    | US      |         |
| Wang, Ruoping      | Austin | TX    | US      |         |
| Reich, Alfred J.   | Austin | TX    | US      |         |

US-CL-CURRENT: 430/5; 430/322

| Full      | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |
|-----------|-------|----------|-------|--------|----------------|------|-----------|-----------|-------------|------|
| Draw Desc | Image |          |       |        |                |      |           |           |             |      |

☐ 5. Document ID: US 20020168578 A1

L10: Entry 5 of 42

File: PGPB

Nov 14, 2002

PGPUB-DOCUMENT-NUMBER: 20020168578  
PGPUB-FILING-TYPE: new  
DOCUMENT-IDENTIFIER: US 20020168578 A1

TITLE: Phase shifting circuit manufacture method and apparatus

PUBLICATION-DATE: November 14, 2002